AMENDMENTS TO THE SPECIFICATION

Please amend page 13 line 19 as follows:

In a preferred embodiment, processor 301 operates to conduct a transaction with memory board [[307]]311, which transaction may be a memory read or memory write operation. The source agent 302 and database 303 at the source site and memory control 305 and CAM 306 at the destination site preferably operate to provide management of the transaction and preservation of data on the progress and status of the transaction so as to minimize latency, avoid duplication of transaction steps, and enable system 300 to possess unambiguous information regarding the status of all transactions pending in system 300 at any given time.

Please amend page 15 line 6 as follows:

reservation response 308 at agent 302, agent 302 retransmits reservation request 307. The time-out period for dropping reservation request 307 from the system is preferably smaller than the time period established in agent 302 for retransmitting the reservation request, thereby ensuring that any prior reservation request packets have been dropped from the system prior to transmission of a duplicate reservation request. In this manner, the inventive mechanism preferably operates to ensure that there will be no more than one reservation request pertaining to the same transaction traveling through network 304 at the same time.

Please amend page 15 line 14 as follows:

In a preferred embodiment, when destination memory control 305 receives reservation request 307, memory control 305 preferably determines whether there is a pre-existing entry in CAM 306 corresponding to reservation request 307. If no corresponding entry is found in CAM 306, memory control 305 preferably concludes that reservation request 307 is being received at memory control 305 for the first time. In this case, the transaction associated with reservation request 307 will preferably be acted upon by writing data to, or reading data from, memory [[307]]311.